

Program Overview

All plenary sessions will take place in **Auditorium EL5** – poster sessions and lunches are organized in the area around the auditorium

| June 27, 2016, Monday | | June 28, 2016 Tuesday | | June 29, 2016 Wednesday | | June 30, 2016 Thursday | |
|-----------------------|---|-----------------------|---|-------------------------|---|------------------------|--|
| Time | Program | Time | Program | Time | Program | Time | Program |
| 8:30-16:00 | Registration | 8:30-17:00 | Registration | 8:30- | Registration | 8:30- | Registration |
| 9:00-11:00 | Tutorial 1: Research on Modern Power Systems supported by Hardware-In-the-Loop Technology | 9:00-9:20 | Opening ceremony Welcome words from NTNU Welcome from Elected PELS President and Immediate Past PELS President | 8:30-9:10 | Keynote speech 3 Bjørn Gustavsen - Frequency-dependent modelling of components in power electronics systems | 8:30-9:10 | Keynote speech 4 Aleks Prodic - Mixed-Signal Control of Emerging Hybrid Converter Topologies |
| | | 9:20-10:00 | Keynote speech 1: Don Tan - Six Basic Characteristics of a Modern Grid | 9:10-10:30 | Oral Session 3: High Frequency Converters | 9:10-10:30 | Oral Session 6: DC-DC Converters |
| 11:00-11:10 | Coffee break | 10:00-10:20 | Coffee break | 10:30-11:00 | Coffee break | 10:30-11:00 | Coffee break |
| 11:10-13:00 | Tutorial 2 Practical Feedback-loop Design of Bus Converters Supplying Regulated Voltage to DC-Input-Port Converters | 10:20-11:00 | Keynote speech 2 Mohamed Belkhatat - Efficiency, Control, and Stability of Power Electronic Based Systems | 11:00-13:00 | Oral Session 4: Model Identification and Hardware-in-the-Loop Techniques | 11:00-13:00 | Poster Sessions |
| | | 11:00-13:00 | Oral Session 1: Stability of AC Power Electronic Systems | | | | |
| 13:00-14:00 | Lunch Location: Kjelhuset (3 min. walk) | 13:00-14:00 | Lunch Location: Kjelhuset (3 min. walk) | 13:00-14:00 | Lunch Location: Kjelhuset (3 min. walk) TC1 Meeting – Room KJL3 | 13:00-14:00 | Lunch Location: Kjelhuset (3 min. walk) |
| 14:00-16:00 | Tutorial 3 Control and Stability Analysis of AC Power Systems with High Penetration of Power Electronic Converters | 14:00-16:00 | Poster Sessions | 14:00-16:00 | Poster Sessions | 14:00-15:20 | Oral Session 7: Advanced Control Techniques for Power Electronics |
| | | | | | | 15:20-15:40 | Coffee Break |
| | | 16:00-17:20 | Oral Session 2: Modeling of Modular Multilevel Converters | 16:00-17:20 | Oral Session 5: Stability of DC Power Electronic Systems | 15:40-17:00 | Oral Session 8: Optimization of Power Electronic Components and Systems |
| | | 17:20-17:40 | Coffee Break | | | 17:00-18:00 | Awards Committee Meeting - Room EL21 (G108) |
| 18:30-22:00 | Welcome Reception with Concert Nidaros Cathedral & Restaurant To Tårn | 17:40-18:40 | Industrial Tutorial: High Performance real Time Simulation of Power Electronics on FPGA. Presented by OPAL-RT | 17:30-19:30 | Industry Seminar and Laboratory Tour with Drink Reception hosted by EGSTON | 19:00-23:00 | Gala Dinner and Best Paper Award Ceremony Banksalen, Kongens gt. 4, Trondheim City Center |

Registration will take place outside **Auditorium EL5** – When the registration desk is not manned, the **COMPEL Secretariat** will be available in room **EL21**