

Seminar: Energy Efficient Design

14th September 2018



IOP Institute of Physics

Approximately one third of the world's energy consumption is electrical energy. The *IEEE Finnish Jt. Chapter of SP&CAS* and the *IOP Finland Chapter* invite you to a seminar on current and future physics and electronics engineering solutions to reduce energy consumption.

The seminar will take place from **9:30 to 16:00 on Friday 14th September 2018, Korkeakoulunkatu 10, 33720 Tampere**, in room TB109, building Tietotalo, at Tampere University of Technology.

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| 09:30 | Registration and coffee/tea |
| 10:00 | Introduction |
| 10:15 | Don Draper, ProPrincipia, USA - ' Hardware Accelerators for Machine Learning Processors in Modular 3D IC with GaN Integrated Voltage Regulators ' |
| 11:00 | Juha Tiihonen, TUT - ' Quantum Dot Cellular Automata - the Future of Binary Computation? ' |
| 11:45 | Lunch |
| 13:00 | Niko Honkonen, Danfoss - ' Energy Efficiency achieved by Variable Frequency Motor Drives ' |
| 13:45 | Coffee/Tea |
| 14:15 | Michel Gillet, Loupedeck - ' From Discobus to Ara - the Tale of Low Power Embedded Networks in Mobile Devices ' |
| 15:00 | Yang Bai, University of Oulu - ' Multi-Source Energy Harvesting for Energy Efficient Wireless Sensor Networks ' |
| 15:45 | Closing remarks |
| 16:00 | End |

The attendance fee, including lunch and refreshments, is € 25, with a discount for IEEE, IOP, Finnish Physical Society members and students.

Everyone interested in the subject is welcome to attend.

Please register in advance at: <https://energyefficientdesign.eventbrite.co.uk>



Tampere University of Technology

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