

Curriculum Vitae

Dr. V.JAYAPRAKASAN, M.E., Ph.D (JNTUA), SMIEEE, LMISTE
Treasurer, IEEE Madras Section (2018 – 2019)
Chairman, IEEE Product Safety Engineering Society
Madras Chapter, India

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DoB: 20th May 1973

Summary

Google Scholar Citation Link: <https://scholar.google.co.in/citations?user=eSIKbdIAAAAJ&hl=en>

- A thorough professional with a rich mixture of over **11 years of Teaching Experience** and **12 years of Industrial Exposure with Research activities**, raring to excel in the field of education and ambitious to create a bright, multi talented, strong younger generation.
- Under my leadership (As a Chair of IEEE Product Safety Engineering Society, Madras Chapter) **IEEE PSES, Madras Chapter, India has been selected as chapter of the year for 2016.** The award comes with a certificate, chapter banner and prize money US\$250.00.
<http://ewh.ieee.org/soc/pses/chapters.html>
- Established **CISCO Networking Academy Programme (CCNA)**
 - **Value added course to the students of GTEC**
 - ❖ As a Netacad Contact / GTEC: Cisco Networking Academy (July 2006 to July 2015), where over more than 250 students have been enrolled and trained through this Cisco Networking Academy. It emphasizes concepts and skills required to design networks, while providing opportunities for practical application and hands-on experience by teaching students how to install, operate and maintain networks. It also helps the students in developing the skills which are necessary to fulfill the job responsibilities of network administrators and network engineers.
- Established **IIRS Outreach Programme in SITAMS, Since 23rd July 2016**
 - ❖ As a Coordinator, organized the IIRS outreach program; which is popular across India, and conducts e-learning Programme (<http://elearning.iirs.gov.in>) on Remote Sensing and Geo information Science by Indian Institute of Remote Sensing (IIRS is a premier Training and Educational Institute) under Indian Space Research Organization (ISRO), Department of Space, Govt. of India.

Education

Ph.D Electronics and Communication (JNTUA, Ananthapuramu, Andhra Pradesh, India)

Progress of Research Work:

- Registered in 2008 & Course works completed on August 2009
- Pre-Submission Synopsis Seminar held on 2nd April 2014
- Thesis approved for submission after Anti Plagiarism Check (18%) on 17th June 2014
- Thesis Submitted on 31st July 2014
- Viva-voce held on 16th March 2016

**M.E. Electronics and Communication Engineering
(Specialization in Communication Systems)**

B. S. Abdur Rahman Crescent Engineering College,
Anna University, Chennai, Tamilnadu, India
June 2006

B.E. Electronics and Communication Engineering (Part-Time)

National Institute of Technology (REC-Tiruchirappalli),
Bharadhidasan University, Tiruchirappalli, Tamilnadu, India
November 1999

Diploma Electronics and Communication Engineering

Thanapandian Polytechnic, Madurai, Tamilnadu, India,
April 1993

- **Won First Prize for academic excellence during the period 1992-1993**

Title of the Research Work: Investigation on Performance of Decimation Filter Structure for Wireless Applications

Name and Designation of the Supervisor:

Dr. M. Madheswaran, M.E., Ph.D (IIT-BHU), M.B.A., (Ph.D)
Chairman - IEEE India EDS Chapter,
Vice Chairman - IEEE India SSCS Chapter,
Principal/Professor,
Dept. of Electronics and Communication Engineering,
Mahendra Engineering College (Autonomous),
Mallasamudram – 637 503, Tamilnadu, India

Professional Experience

August 2015 to till

Associate Professor, Department of Electronics and Communication Engineering, Sreenivasa Institute of Technology and Management Studies (Autonomous), Chittoor - 517127, Andhra Pradesh, India.

- **Coordinator – IIRS Outreach Programme (Basics of Remote Sensing, GIS and GNSS)**
- **As an Indian Examiner, Reviewed Ph.D Thesis from Anna University, Chennai.**
- **Reviewer in Springer (Circuits, Systems and Signal Processing) Journal**
- **Reviewer in Allied Academies Journals (Annexure - I Journal in Anna University)**
- **Organized a Two-day's workshop on "Importance of Switching and Routing Concepts in Computer Networks Using Cisco Packet Tracer" on 11th and 12th August 2017 at SITAMS.**
- **Organized a Two-day's workshop on "Recent Trends and Challenges in Communication and Networking Using Cisco Packet Tracer" on 12th and 13th August 2016 at SITAMS.**
- **Organized a Two-day's workshop on "Electronic Circuit Design – Artwork and Practice" on 3rd and 4th March 2016 in association with IEEE PSES (Madras Chapter) at SITAMS.**
- **Organized a Two-day's workshop on "PCB Design and Soldering Practices" on 26th and 27th February 2016 at SITAMS.**

June 2006 to July 2015

Professor, Department of Electronics and Communication Engineering, Ganadipathy Tulsi's Jain Engineering College, Vellore – 632102, Tamilnadu, India

- **PG Coordinator – M.E. Communication and Networking**
- **Established CISCO Networking Academy Programme (CCNA)**
 - Value added course to the students of GTEC
- **Established ANNA EDUSAT Remote Class Room**
- Instrumental in starting new **PG Programme** with specialization in **Computer & Communication** in the department
- Established Networks Lab and PG laboratories with the advanced equipment and software's
- Co-ordinated post graduate and under graduate project works.
- Taught many core papers like, Digital signal processing, Communication Theory, Mobile Communication, Computer Networks, High Performance Communication Networks and Wireless communication, etc.,
- As professor, actively providing valuable guidance for the students to plan for their career growth.

Jan. 2005 to May 2006

Consultant, M/s Digi Soft Solutions, Tiruchirappalli.

(The company manufactures and Supplies, C & I Equipment for Boilers to NTPC and Electricity Boards all over India, Energy Savers for domestic applications in single phase and three phase, Building Automation, Access Control, Fire Alarm Monitoring and CCTV)

Nature of Work: Technical Support

June 2002 to Sep. 2004

Product Manager, M/s DSM Energy Ventures Private Ltd., Tiruchirappalli.

Aug. 1997 to May 2002

Product Engineer, M/s Panel Electronics India Private Ltd., Tiruchirappalli.

(The company manufactures and Supplies C & I Equipment for Boilers to M/s National Thermal Power Corporation and Electricity Boards all over India. In addition to manufacturing the above cited items the company manufactures Dust Lighting Systems with Energy Saver, which saves energy to an extent of 28%, Security Systems for Residential Apartments and Banks)

Nature of Work:

- Research and Development
- Erection and Commissioning of UV, Safe Flame and UV to Visible type Flame Scanners, High Energy Igniters for Boilers.
- Planning and Estimation of all Projects. Interacting with clients and suppliers.
- Interaction with the engineers and assisting various projects.
- Overall Supervision on all areas. Like production process, quality, materials availability & Purchase.

Jan. 1994 to July 1997

Technical Assistant, M/s Aerotronics, Tiruchirappalli.

(The company manufactures and Supplies C & I Equipment for Boilers to National Thermal Power Corporation and Electricity Boards all over India)

Nature of Work:

- Artwork Drafting and PCB Design
- Assembling and testing of PCB's for boiler control equipment's

Aug. 1993 to Dec. 1993

Trainee, M/s Microwave Product India Ltd., Coimbatore.

(The company manufactures Microwave Oven, Battery Car and AC & DC Drives)

Nature of Work: Had Training in R&D and Battery Production

M.E. Project Work:

Project Title : **Energy Monitoring, Control and Management using GSM Modem**
Organization : **Airport Authority of India, Chennai**
External Supervisor : **Mr.Thulasiraman, AGM/Airport Authority of India, Chennai**

Additional Responsibilities

- **SITAMS : IIRS Outreach Programme Coordinator (Since 23rd July 2016)**
About IIRS:
 - Indian Institute of Remote Sensing (IIRS) under Indian Space Research Organization (ISRO), Department of Space, Govt. of India is a premier Training and Educational Institute set up for developing trained professionals in the field of Remote Sensing, Geoinformatics and GPS Technology for Natural Resources, Environmental and Disaster Management. Formerly known as Indian Photo-interpretation Institute (IPI), founded in 1966, the Institute boasts to be the first of its kind in entire South-East Asia.
 - The Institute has enhanced its capability and evolved many training & education programmes that are tuned to meet the requirements of various target groups, ranging from fresh graduates to policymakers including academia.
 - IIRS outreach program is popular across India, and conducts e-learning Programme (<http://elearning.iirs.gov.in>) on Remote Sensing and Geo information Science.
- **Chief Superintendent for TANCET 2013 Examination**
- **Netacad Contact / GTEC: Cisco Networking Academy (July 2006 to July 2015)**
 - Cisco Networking Academy at GTEC has been successfully operating since 2006; where over more than 250 students have been enrolled since inception.
 - Through this Cisco Networking Academy, GTEC offers CCNA Exploration course and it emphasizes concepts and skills required to design networks, while providing opportunities for practical application and hands-on experience by teaching students how to install, operate and maintain networks.
 - It also helps the students in developing the skills which are necessary to fulfil the job responsibilities of network administrators and network engineers.

Invited talks/Lectures

- As a Resource Person, handled session “**Need for Standards and IEEE Standards related to Wired and Wireless Communication**” on 5th December 2017 in the **Series of Technical Talk on “Codes and Standards Week”** at Ambedkar Auditorium, Vellore Institute of Technology, Vellore, Tamilnadu, from 4th - 9th December 2017 Organized by School of Electrical Engineering in association with IEEE Product Safety Engineering Society and IEEE Industry Applications Society, Madras Chapter.
- Handled the sessions in National Level Two-day’s workshop on “**Importance of Switching and Routing Concepts in Computer Networks Using Cisco Packet Tracer**” on 11th and 12th August 2017 at SITAMS Chittoor, Andhra Pradesh.
- As a Resource Person, handled Sessions in One day National Level Workshop on “**Importance of Multirate Signal Processing for Digital Communications**” on 9th September 2017 at Syed Ammal Engineering College, Ramanathapuram – 623502, Organized by Department of ECE in association with IEEE Communications Society and IEEE PSES (Madras Chapter).
- Delivered a technical seminar talk on “**Importance of Multirate Signal Processing for Secure Communication**” on 9th August 2017 at Adhiparasakthi College of Engineering GB Nagar, Kalvai-632 506.
- Delivered a technical seminar talk on “**Challenges and Importance of E-waste Management and Environment Issues**” on 20th April 2017 at Narayana Engineering College, Nellore – 524004, Andhra Pradesh.
- As a Chief Guest, inaugurated and delivered the key-note address on “**Challenges and Importance of Electronics Waste Management**” in 4th International Conference held at SKR Engineering College, Poonamallee, Chennai – 600123, On 31st March 2017.
- As a Chief Guest, inaugurated and delivered the key-note address on “**The advancement of New Technology – Positive or Negative**” in 3rd International Conference held at Vetri Vinayaka College of Engineering and Technology, Thottiam – 621 215, Trichy Dt. On 25th March 2017.
- As a Resource Person, handled Sessions in Two-days National Level Workshop on “**Recent Trends and Challenges in Communication and Networking Using Cisco Packet Tracer**” on 20th & 21st February 2017 at Mahendra Engineering College (Autonomous), Mallasamudram, Namakkal - 637503, Organized by Department of ECE in association with IEEE Product Safety Engineering Society (Madras Chapter).
- Delivered a technical seminar talk on “**Challenges in E-Waste Management**” on 18th February 2017 at Syed Ammal Engineering College, Ramanathapuram – 623502.
- As a Resource Person, handled Sessions in Two-days National Level Workshop on “**Importance of Switching and Routing in Computer Network using Cisco Packet Tracer**” on 17th & 18th February 2017 at Syed Ammal Engineering College, Ramanathapuram – 623502, Organized by Department of ECE and CSE, in association with IEEE Communications Society and IEEE PSES (Madras Chapter).
- As a Resource Person, handled Sessions in Two-days National Level Workshop on “**Networking, Switching and Routing – A closer Look**” on 8th & 9th August 2016 at Vel Tech Dr. RR & Dr.SR Technical University, #42, Avadi - Vel Tech Road, Avadi, Chennai – 600062 Organized by Department of CSE, Veltech University in association with Computer Society of India, IEEE Computer Society and IEEE PSES (Madras Chapter).

- Delivered a technical seminar talk on “**Understanding TCP/IP Addressing and Subnetting Concepts**” on 20th August 2016 at St .Joseph College of Engineering, Chennai - 600119.
- Delivered a technical talk on “Network Fundamentals and Addressing Concepts in Communication and Networking” in Two-days National Level Workshop on “**Recent Trends and Challenges in Communication and Networking Using Cisco Packet Tracer**” on 12th & 13th August 2016 at Sreenivasa Institute of Technology and Management Studies (Autonomous), Chittoor, Andhra Pradesh.
- Delivered a technical seminar talk on “**Challenges in Communication and Networking**” on 9th August 2016 at Aarupada Veedu Institute of Technology, Vinayaka Missions University, Kanchipuram – 603104.
- Delivered a seminar talk on “**Easy Ways to Save the Planet**” in **IEEE (Madras Section) Sponsored National Level Technical Seminar** on 9th May 2016 at Jayaraj Annapackiam CSI College of Engineering, Nazareth - 628 617.
- Delivered a lecture on “**Circuit Design on PCB**” in Two-days National Level Workshop on “**Electronic Circuit Design – Artwork and Practice**” on 3rd & 4th March 2016 at Sreenivasa Institute of Technology and Management Studies (Autonomous), Chittoor, Andhra Pradesh.
- Delivered a seminar talk on “**5S Concepts**” in **IEEE (Madras Section) Sponsored National Level Technical Seminar** on 15th December 2015 at Ganadipathy Tulsi’s Jain Engineering College, Vellore, Tamilnadu.
- Delivered a motivational talk on “**IEEE Membership Benefits**” in **IEEE (Madras Section) Sponsored National Level Technical Seminar** to both the students and faculty members of Priyadharshini Engineering College, Vaniyambadi, Tamilnadu on 28th October 2015.
- Delivered a Guest lecture on “**Electrical Safety Precautions and Basic concept of Science and Technologies**” in STAP (Students Technology Awareness Programme on 7th July 2015 at Government Higher Secondary School, Kaniyambadi, Vellore, Tamilnadu.
- Delivered a lecture on “**Network Fundamentals and Routing Protocols using Packet Tracer**” 19th March 2015 at Sri Krishna Institute of Technology, Chennai, Tamilnadu.
- Delivered a lecture on “**Applications of MATLAB in Communication System & VLSI Implementation using MATLAB**” in **TEQIP-II sponsored one week Faculty Development Programme** on “**Recent MATLAB Techniques to Engineering Applications**” from 12th to 18th May 2014 at EEE Department of Government College of Engineering, Bargur, Tamilnadu on 16th May 2014.
- Delivered a lecture on “**Network Fundamentals and Routing Protocols**” in Two-days National Level Workshop on “**Computer Networks**” on 24th & 25th March 2014 at Ganadipathy Tulsi’s Jain Engineering College, Vellore, Tamilnadu.

Publications in Journals

1. **V.Jayaprakasan**, C.Kumar, K.Pavithra and V.Ram Priyan, “Comparative Analysis of Interpolation/Decimation FIR Filter Structures for WLAN-b and WLAN-g Applications,” *International Journal for Innovative Research in Science & Technology*, Volume 2, Issue 12, ISSN (online): 2349-6010, May 2016.

2. **V.Jayaprakasan**, K.Pavithra, V.Ram Priyan and Sujitha.P, "Performance Analysis of Different Decimation Filter Structure Realizations using Multistage Approach for GSM Applications," *International Journal of Applied Engineering Research, Special Issue, Volume 10, No.09 (2015), April 2015 (Annexure – II Journal in Anna University)*
3. **V.Jayaprakasan** and M.Madheswaran, "FPGA Implementation of FIR based Decimation Filter Structure for WiMAX Application," *International Journal of Advanced Research in Computer and Communication Engineering, Volume 2, Issue 7, July 2013.*
4. M.Madheswaran and **V.Jayaprakasan**, "Implementation and Comparison of Different CIC Filter Structure for Decimation," *ICTACT Journal on Communication Technology, Volume 04, Issue: 02, June 2013.*
5. D.Kumuthavijay, J.Nandhini and **V.Jayaprakasan**, "Implementation of Efficient Audit Service Outsourcing for Data Integrity by Interfacing the Mobile Device in Clouds," *International Journal of Computer Applications (0975-8887), Volume 67 – No.20, April 2013.*
6. P.Durai Saravanan and **V.Jayaprakasan**, "Design and Implementation of Efficient CIC Filter Structure for Decimation," *International Journal of Computer Applications (0975-8887), Volume 65 – No.14, March 2013.*
7. R.Manigandan and **V.Jayaprakasan**, "Spectrum Sensing and Security in Cognitive Radio," *International Journal of Computer Applications (0975-8887), Volume 66 – No.23, March 2013.*
8. M.Nemaraj, **V.Jayaprakasan** and V.Senthil Kumar, "Power Optimization Technique for Sensor Network," *International Journal of Computer Applications Technology and Research, Volume 2 – Issue 3, 255 – 260, 2013.*
9. P.Iswarya, D.Santhosh Kumari and **V.Jayaprakasan**, "Improvisation of MAC Protocol for Wireless Sensor Network," *International Journal of Electronics and Communication and Computer Engineering, Volume 4, Issue 3, ISSN (Online): 2249-071X, ISSN (Print): 2278-4209, 2013.*
10. M.Boopathy, **V.Jayaprakasan** and Y.J.Nazeer Ahmed, "Performance Analysis for Parallel MRA in Heterogeneous Wireless Networks," *International Journal of Computer Applications Technology and Research, Volume 2 – Issue 3, 329 – 334, 2013.*
11. S.A.M.Felicita, B.Thamizhezhil and **V.Jayaprakasan**, "Efficient way of Communication using Fuzzy Theory," *Journal of Science and Engineering, Volume 2 (1), 2013, 31-38.*
12. **V.Jayaprakasan** and M.Madheswaran, "Design of Efficient Polyphase Multistage FIR Filter with Memory Saving Structure for Decimation," *European Journal of Scientific Research, Vol. 93 No 2 December, 2012, pp.289-300.*

Publications in Conferences

1. **V.Jayaprakasan**, K.Pavithra, V.Ram Priyan and Sujitha.P, "Performance Analysis of Different Decimation Filter Structure Realizations using Multistage Approach for GSM Applications," *International Conference on Engineering Technology and Science, (ICETS'15), March 5th and 6th, 2015, Rasipuram, Tamilnadu, India.*
2. M.Madheswaran and **V.Jayaprakasan**, "Design of Efficient Decimation Filter Structure for WiMAX Applications with Memory Saving Approach," *IEEE International Symposium on Signal Processing and Information Technology, (ISSPIT'2014), December 15-17, 2014, Noida, India.*

3. M.Madheswaran and **V.Jayaprakasan**, “FPGA Realization of Efficient Decimation Filter Structure using CIC structure for WLAN Applications,” IEEE Asia Pacific Conference on Circuits and Systems (APCCAS’2014), November 17-20, 2014, Okinawa, Japan.
4. **V.Jayaprakasan** and M.Madheswaran, “Implementation of Two Stage FIR Decimation Filter Structure,” 2nd International Conference on Communications and Signal Processing (ICCSP’2013), 1st – 3rd April, 2013, Ongole, India, pp. 99-10. (**Achieved Best Paper Award**)
5. **V.Jayaprakasan** and M.Madheswaran, “Cascading Sharpened CIC and Polyphase FIR Filter for Decimation Filter,” 2nd International Conference on Advances in Electrical and Electronics Engineering (ICAEEE’2013), March 17–18, 2013, Dubai (UAE), pp.148-154.
6. **V.Jayaprakasan** and S.Vijayakumar, “Evaluation of the Conventional Vs. Ancient Computation methodology for Energy Efficient Arithmetic Architecture,” International Conference on Process Automation, Control and Computing (IEEE – PACC 2011), July 20–22, 2011, Coimbatore, India.

Faculty/Staff Development Programmes Attended

- **AICTE Sponsored SDP** on “Application of Digital Filters in DSP Problems” held at Adhiparasakthi Engineering College, Melmaruvathur, Tamilnadu, India, during May 3 – 15, 2012 (Two weeks).
- **Faculty Development Program** on “EDA Tools for VLSI Design and Signal Processing” held at SSN College of Engineering, Kalavakkam, in association with Tamilnadu State Council for Science and Technology, Chennai, November 15–18, 2010.
- **AICTE Sponsored SDP** on “IPv6 Migration and Co-existence” held at Sri Krishna College of Engineering and Technology, Coimbatore, Tamilnadu, India, during May 17– 29, 2010 (Two weeks).
- **AICTE Summer school** on “Technology Development and Management”, held at NITTTR, Chandigarh, India, during August 03 – 14, 2009 (Two weeks).

Conference, Seminars and Short Term Courses Attended

- **Workshop on Network Simulation Tools NS2 & NS3** held at Panimalar Institute of Technology, Chennai, Tamilnadu, May 21 & 22, 2015.
- **Xilinx System Generator Tool Workshop** held at Karunya University, Coimbatore, Tamilnadu, January 18 & 19, 2013.
- **Semiconductor solutions for wireless communication Workshop** held at Anna University Chennai, Tamilnadu in association with Texas Instruments India, October 05 & 06, 2012.
- **Xilinx DSP Design flow using System Generator Workshop** National Institute of Electronics & Information technology, Chennai, Tamilnadu, September 13 & 14, 2012.
- **FinFET Circuit Design – An Alternate for CMOS Design Workshop** held at Muthayammal Engineering College in association with IEEE India EDS Chapter, Rasipuram, Tamilnadu, April 28, 2012.
- **SDR – The Future Radio Seminar** Organized by Electronics Division, VIT University, Chennai Campus, Tamilnadu, February 23, 2012.
- **International Conference on 4G Wireless Computer Networks** conducted by Kamban Engineering College, Thiruvanamalai, March 6, 2011.

- **MIMO - OFDM Wireless Systems Workshop** held at SRM University, Kancheepuram, Tamilnadu, August 28, 2010.
- **International Workshop "The past & future of Integrated Circuits Technologies"** Conducted by SKP Engineering College, Thiruvanamalai, Tamilnadu in association with IEEE EDS, November 4 & 5, 2009.
- **TCAD and IC CAD Technologies and FPGA Design using Silvaco and Tanner Tools Seminar** Conducted by Muthayammal Engineering College, Rasipuram, Tamilnadu, September 20, 2008.
- **EDUSAT e-learning Programme** organized by Department of Telecommunications at DSCE, Bangalore and Dayananda Sagar College of Engineering, Bangalore, Karnataka, 2005.
- **Reliable Soldering Practices Training**, held at Electronic Test and Development Centre, Chennai, Tamilnadu, 2000.

Membership in Scientific and Professional Societies

- **GTEC: IEEE Student Branch Counselor (August 2014 to July 2015)**
Providing good exposure among the students in the field of engineering and as IEEE Student branch counselor organized many technical events.
- **Senior Member in IEEE (92295009)** (Institute of Electrical and Electronics Engineering)
 - Member in Communications Society
 - Member in Product Safety Engineering Society
- **Chairman - IEEE Product Safety Engineering Society (Madras Chapter)**
 - Since 13th May 2015 in Madras Section
- **Treasurer - IEEE Communications Society (Madras Chapter)**
 - Since 1st January 2016 in Madras Section
- **Life Member in ISTE** (Indian Society for Technical Education)
- **Associate Member in IRED** (Institute of Research Engineers & Doctors)

References:

1. **Dr. M. Madheswaran, M.E., Ph.D (IIT-BHU), M.B.A., (Ph.D)**
Chairman - IEEE India EDS Chapter,
Vice Chairman - IEEE India SSCS Chapter,
Principal/Professor,
Dept. of Electronics and Communication Engineering,
Mahendra Engineering College (Autonomous),
Mallasamudram – 637 503, Tamilnadu, India
2. **Dr. S. Kaja Mohideen, M.E., Ph.D.,**
Professor & Dean,
School of Electrical and Communication Sciences,
B.S.Abdur Rahman Crescent University,
Seethakathi Estate, Vandalur,
Chennai – 600048, Tamilnadu, India

(V.Jayaprakasan)