N.B.: (1) Question No. 1 is compulsory.
(2) Attempt any four out of the remaining six questions.
(3) Assume any suitable data wherever required.

1. You need to design a database for an art gallery. The database schema must keep information about artists, their names, (which are unique), birth places, age and style of art and photograph. For each piece of artwork, the artist, the year it was made, its unique title, its type of art (e.g., painting, sculpture, photograph), and its price, along with picture (thumbnail) must be stored. The database also stores information about customers. For each customer, database stores the person's unique name, address, and total amount of money spent in the gallery and the artist and type of art the customer tends to like (can be a text):
   (a) Draw an EER dia for the system 6
   (b) Map the EER to Relations 8
   (c) Take two typical queries and write them in SQL. 6

2. (a) Explain different architectures for parallel database. 10
   (b) State comparison of RDBMS, OODBMS, ORDBMS. 10

3. (a) Explain data fragmentation, replication and allocation technique for distributed database design. 10
   (b) Give an overview of 3-Tier client server architecture. 10

4. (a) What is SQL 3? Write in detail about features of SQL 3. 10
   (b) What is well formed and valid XML document? With example explain what is XML schema file? 10

5. (a) Explain in detail about heuristic approach to query optimization. 10
   (b) Explain the method for implementing the SELECT operation. 10

6. (a) Explain conceptual database design in database design and implementation process. 10
   (b) Explain concurrency control in distributed database. 10

7. Write a short notes on any two:
   (a) Specialization and Generalization 20
   (b) Joins in SQL.
   (c) Measures of query cost.
N.B.: (1) Question No. 1 is compulsory.
   (2) Solve any four questions from the remaining.
   (3) Figures to the right indicate marks.
   (4) Assume data if necessary.

1. (a) Explain addressing modes of 8085 microprocessor with example.  
     (b) What is segmented memory? State the advantages of it w.r.t 8086 microprocessor.  

2. (a) What is meant by DMA? Show interfacing of 8237/57 with 8088 and explain.  
     (b) Explain following instructions with one example each (w.r.t 8086).
        (i) SAL     (ii) TEST    (iii) STOS    (iv) CMP    (v) JC.

3. (a) Explain the operation of IC 8259 with block diagram.  
     (b) Write an assembly language program for 8086 to exchange the blocks of 1 KB located at $0100H$ and $0200H$ using storing instructions.

4. (a) Explain Assembler directives of 8086.  
     (b) What are the various modes of operation of 8255 PPI?  

5. (a) Explain the addressing modes of 8085 microprocessor.  
     (b) Draw the timing diagram and explain for:
         (i) Memory read in minimum mode  
         (ii) Memory write in maximum mode.

6. (a) Explain Interrupts of 8086 in detail.  
     (b) (i) Explain Generation of Address and data Bus.  
              (ii) Differentiate between memory mapped I/O and I/O mapped I/O.

7. Write short notes on any three:—  
   (a) RS 232 serial Interface Standard  
   (b) Memory Banking in 8086  
   (c) 8234 Clock Generator  
   (d) 8288 Bus Controller.
N.B. (1) Question No. 1 is compulsory.
(2) Solve any four questions out of the remaining.
(3) Marks assigned to the sub-questions as indicated.

1. a) With a neat diagram compare the uses and functions of different hardware components/devices used in an internetwork. (10)
   b) Compare virtual circuits and datagram subnets and show their diagrammatic representation during congestion control. (10)

2. a) What are the advantages of a variable length frame over fixed length frames. Explain the different framing methods. (10)
   b) Explain FDMA, TDMA and CDMA (10)

3. a) Derive the efficiency of Pure Aloha protocol (10)
   b) A receiver receives the code 11001100111. When it applies the Hamming code algorithm the result is 0101. Which bit has the error? What is the correct Hamming code? (10)

4. a) Describe the IPv4 header format in detail. (10)
   b) Explain the three protocol scenarios for establishing a connection using a 3-way handshake in TCP (10)

5. a) Explain DVR routing algorithm and mention the drawbacks of the algorithm when put into practice (10)
   b) Explain the working of Transactional TCP (10)

6. a) List the design features to be taken care of as congestion prevention policies in the different layers of network (10)
   b) Draw the layered structures and compare the two network reference models – OSI and TCP/IP (10)

7. Write notes on: (any two) (20)
   a) SONET
   b) Ethernet frame formats
   c) ADSL
   d) Satellite Communication
N. B.: (1) Question No. 1 is compulsory.
(2) Attempt in all five questions.
(3) Figures to the right indicate full marks.
(4) Draw neat circuit diagrams/blocks diagrams wherever necessary.

Q 1. Attempt any four:
   a. Compare PCM and delta modulation
   b. Compare between antipodal and Orthogonal signal.
   c. Explain ergodic process.
   d. Explain Inter Symbol Interference
   e. What is the function of match filter in communication system

Q 2. a) Explain Quadrature Amplitude shift keying (transmitter, Receiver) in detail
   b) Explain Central Limit Theorem (CLT)
   C )What is companding ? Explain laws of compression.

Q No.3. a) Define cumulative distribution function and probability distribution function. List their Properties.
   b) The required power at the receiving antenna is $10^{-6}$ W. The gain of the transmitting and receiving antennas are 20 dB each. The frequency of the carrier is 5GHz and distance between two antennas is 10Km. Determine power to transmitted by transmitter

Q 4 a) Generator polynomial for a (15,7) Cyclic code is $g(x) = 1 + x^4 + x^6 + x^7 + x^8$
   i) Find the code vector (in systematic form) for message polynomial $D(x) = x^2 + x^3 + x^4$
   ii) Assume that first & last bits of the code vector for $D(x) = x^2 + x^3 + x^4$ suffer from transmission errors. Find the error syndrome of received code vector.
   b) What is matched filter? Derive the expression for minimum probability of error of the matched filter.
Q.No.5 a) What is Binary Symmetric channel (BSC)? Explain optimum receiver algorithm for binary symmetric channel.

b) A key is given by the sequence of $K_1, K_2, \ldots, K_{29}$, where $K_i$ is the shift number of $i^{th}$ character. Its value lies in the range of 1 to 25. All the possible keys are equally likely. Calculate unicity distance if English language is used in encryption.

Q.No.6 a) A Delta modulation system is designed to operate at 3 times the Nyquist rate for a Signal with 3 KHZ bandwidth. The quantizing step size to 250 mv.

i) Determine the maximum amplitude of 1KHz input sinusoid for which delta modulator does not show slope overload.

ii) Determine the post filtered output SNR for the signal of part (i).

b) Explain viterbi algorithm.

Q.No.7 Write short Note (any four):

a. Effect of noises on communication system.

b. Signature authentication using public key crypto system.

c. Image and data compression

d. Modified Deobinary Encoder

e. Equilizers.