IEEE Consumer Electronics Society Meetup:
Cypress Semiconductor Solutions for IoT and Wearables

Dave Blevins – Arrow Electronics Field Applications Engineer
20 June 2016
Today, we will discuss:

- **Programmable System-on-Chip** (PSoC)
- **Bluetooth Low Energy** (BLE, also known as Bluetooth Smart)
- **Energy Harvesting** – capturing tiny amounts of power over time and doing interesting things with it
Arrow Electronics

- Founded in 1935 in NYC, selling AM radios
- Today: A global provider of products, services and solutions to industrial and commercial users of electronic components and enterprise computing solutions
- Supply channel partner for over 100,000 original equipment manufacturers, contract manufacturers and commercial customers
- 2015 sales of $23.28 billion
- 460 locations serving over 85 countries
Cypress Semiconductor

Cypress + Spansion = New $1.6B company
No. 1 in SRAMs, No. 1 in NOR Flash
No. 3 in MCUs and memories for the automotive market
First to market USB Type-C controllers with Power Delivery
PSoc® BLE solution to drive traction in IoT, wearables
Traveo® MCUs: First to market ARM®-based 3-D Graphics controller for automotive cluster (dashboard) displays
Technology leadership in Power Management ICs (PMICs)
Cypress-Spansion Merger Expands Leadership in Embedded Systems

No. 1 in SRAM, No. 1 in nvSRAM, No. 1 in F-RAM™
30+ years memory and 15 years of PSoC experience

No. 1 in Flash Memory for Embedded Markets
No. 2 in Japan MCU, well-positioned in Analog

SYNERGISTIC MARKETS: AUTOMOTIVE, INDUSTRIAL, CONSUMER, COMMUNICATIONS

PSoc  Touch  USB  nvRAM  F-RAM  SRAM

NAND  NOR  Software  SoC  MCU  Analog

Global Embedded Systems Leader

Memory for Embedded Systems: SRAM and Flash + MCU, Analog, SoC, USB, Touch = Market Leadership in Embedded Systems
Cypress to Acquire Broadcom’s Wireless IoT Business

A Quantum Leap for Its IoT and Wireless Efforts

April 28, 2016
Leading Embedded Systems Portfolio

Acquisition accelerates embedded system connectivity with “2018 wireless technology”
Three main wireless standards, Wi-Fi, Bluetooth, ZigBee on state-of-the-art, low-power 40-nm and 28-nm CMOS processes

Broad product portfolio for embedded processing
Low-power ARM-based PSoC with programmable analog and digital enables rapid embedded system design
Highest-performance ARM Cortex®-M4 MCU devices with advanced connectivity (Ethernet, CAN-FD, USB Type-C)
Broadest memory selection from any supplier

Acquisition adds state-of-the-art wireless connectivity for next-generation Automotive, Industrial and Internet of Things embedded systems
Diverse Customer Base Requires Scalable IoT Platform and Ecosystem

Wireless Internet Connectivity for Embedded Devices (WICED) platform
Software Development Kit enables the addition of wireless and cloud connectivity to any embedded device
Supports all wireless IoT connectivity standards (Wi-Fi, Bluetooth, ZigBee)
11K+ WICED Software Development Kit registered users

Partner Ecosystem delivers end-to-end solutions based on the WICED platform
Partners provide software, mobile applications, cloud connectivity, wireless modules and system development

Module Makers: Murata, SPIL, USI ...
Value Added Resellers (VARs): Lantronix, LM Technology ...
Technology Partners: Particle, Ayla Networks ...
ODMs: Jabil, Chicony ...

Cypress has an established global channel and support model for 30K+ customers
46 distributors worldwide with over 700 branches
42K+ PSoc Creator Software Development Kit registered users
10K+ Customer engineers trained per year

Leverage Cypress global channel and support model to accelerate WICED platform adoption to profitably serve the broad, diverse IoT customer base
Programmable System-on-Chip with Bluetooth Low Energy
Wearables

Cypress offers a complete portfolio of the industry’s smallest, lowest-power solutions for wearable electronics with intuitive user interfaces and fast time-to-market

**CapSense controllers:** The industry’s smallest, lowest-power devices with superior SNR and waterproofing

**PSoC 4 and PRoC2 Bluetooth Low Energy (BLE) Solutions** Easy-to-use, ultra-low-power wireless connectivity

**Low-Power (MoBL®) Asynchronous SRAMs:** Memories that buffer data to reduce radio usage and extend battery life

**F-RAMs** High-reliability memories that store the most vital data—“black box” data—with the lowest possible energy
Design Example: Fitness Monitor

Wearable Fitness Monitor by Jawbone

Block Diagram

PSoc 4 BLE One-Chip Solution

IDAC

SAR

BLE System

RTC

TCPWM

Vibration Motor

RF Link

Thermistor

Humidity Sensor

Pressure Sensor

Battery Voltage

Accelerometer

Antenna

Pressure Sensor

Battery Voltage

Accelerometer

Antenna

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PSoC: Programmable System-on-Chip

PSoC is the world’s only programmable embedded **system-on-chip** integrating an MCU core, **Programmable Analog Blocks**, **Programmable Digital Blocks**, **Programmable Interconnect and Routing**, and **CapSense**

**PSoC Value**
- Reduced components = Reduced BOM cost
- Smaller BOM = Better quality, smaller footprint
- Simpler, smaller system = Fast time-to-market

**Product Highlights**
- A decade of explosive growth: >2 billion units shipped
- Thousands of active PSoC customers

<table>
<thead>
<tr>
<th>Feature</th>
<th>PSoC 1</th>
<th>PSoC 3</th>
<th>PSoC 4</th>
<th>PSoC 5LP</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Processing Core</strong></td>
<td>8-bit M8C (4 MIPS)</td>
<td>8-bit 8051 (33 MIPS)</td>
<td>32-bit ARM Cortex-M0 (43 DMIPS(^1))</td>
<td>32-bit ARM Cortex-M3 (100 DMIPS(^1))</td>
</tr>
<tr>
<td><strong>Code Storage</strong></td>
<td>Up to 32KB</td>
<td>Up to 64KB</td>
<td>Up to 256KB</td>
<td>Up to 256KB</td>
</tr>
<tr>
<td><strong>Connectivity</strong></td>
<td>FS USB 2.0, I(^2)C, SPI, UART</td>
<td>FS USB 2.0, I(^2)C, SPI, UART, CAN, LIN, I(^2)S</td>
<td>I(^2)C, SPI, UART, I(^2)S</td>
<td>FS USB 2.0, I(^2)C, SPI, UART, CAN, LIN, I(^2)S</td>
</tr>
<tr>
<td><strong>I/Os (w/CapSense)</strong></td>
<td>Up to 64</td>
<td>Up to 72</td>
<td>Up to 55</td>
<td>Up to 72</td>
</tr>
<tr>
<td><strong>AFE(^1) Resolution</strong></td>
<td>8-14 bit</td>
<td>12-20 bit</td>
<td>12-bit</td>
<td>12-20 bit</td>
</tr>
</tbody>
</table>

\(^1\) Dhrystone MIPS (Million Instructions Per Second)
The One-Chip PSoC Embedded Design Platform

This PSoC “Lego” image illustrates how designers use free, embedded PSoC Components to create one-chip solutions.
PSoC Terms

Programmable Analog Block
- A hardware block that is configured using PSoC Components to create Analog Front Ends (AFEs), signal conditioning circuits with opamps and filters
- Includes Continuous Time Blocks, analog-to-digital converters (ADCs) and digital-to-analog converters (DACs)

Continuous Time Block (CTB)
- A Programmable Analog Block that is used to implement continuous time analog circuits such as opamps and programmable gain amplifiers (PGAs)

Programmable Digital Block
- A hardware block that is configured using PSoC Components to implement custom digital peripherals and glue logic
- Includes Universal Digital Blocks, Serial Communication Blocks (SCBs) and TCPWMs
PSoC Terms

**Universal Digital Block (UDB)**
- A PSoC Programmable Digital Block that contains two programmable logic devices (PLDs), one programmable datapath with an arithmetic logic unit (ALU), one status register and one control register
- Configured in PSoC Creator using PSoC Components, or with the graphical UDB editor, or using Verilog code

**Serial Communication Block (SCB)**
- A PSoC Programmable Digital Block that is configurable as a UART, SPI or I2C interface

**Timer, Counter, PWM (TCPWM) Block**
- A PSoC Programmable Digital Block that is configurable as a 16-bit Timer, Counter, PWM or quadrature decoder
PSoC Terms

CapSense®

• Cypress’s third-generation touch-sensing user interface solution that “just works” in noisy environments and in the presence of water
• The industry’s No. 1 solution in sales by 4x over No. 2

Programmable Interconnect and Routing

• Connects the Programmable Analog Blocks, Programmable Digital Blocks and I/Os
• Enables flexible connections of internal analog and digital signals to internal buses and external I/Os
PSoC Terms

PSoC Creator™
• PSoC 3, PSoC 4, PSoC 5LP and PRoC BLE Integrated Design Environment: Software that installs on your PC that allows:
  Concurrent hardware and firmware design of PSoC systems, or
  PSoC hardware design followed by export to popular IDEs

Components
• Free embedded ICs represented by an icon in PSoC Creator software
• Used to integrate multiple ICs and system interfaces into one PSoC
• Dragged and dropped as icons to design systems in PSoC Creator

Component Configuration Tools
• Simple graphical user interfaces in PSoC Creator embedded in each Component
• Used to customize Component parameters as shown to the right
PSoC Creator Enables Complete System Design

BLE Heart Rate Monitor Example Project With a Custom AFE Shown in the PSoC Creator IDE

1. Explore the library of 100+ Components
2. Drag and drop Component icons to complete your hardware system design in the main design workspace (e.g., use the BLE Component for Bluetooth Smart designs)
3. Configure Components using the Component Configuration Tools
4. Co-design your application firmware and hardware in the PSoC Creator IDE
Actual PSoC Creator IoT System Design
(Complete Production Design)
BLE Is the Industry’s Choice for Short-Range, Low-Power Wireless

BLE’s low-power consumption and new features enable it to address a wide range of applications

• BLE transmits information at a low data rate
• BLE connections are quick and transient, enabling connection, data transmission and disconnection in less than 3 ms
• Bluetooth 4.2 improves security and increases the maximum data rate to 800 Kbps

The market for BLE products is expanding rapidly

• The 149 million Bluetooth Smart devices shipped in 2014 will grow at a CAGR of 43% to 1.4 billion by 2019
• The 2.7 billion Bluetooth Smart Ready devices shipped in 2014 will grow at a CAGR of 10% to 4.4 billion by 2019

1 Source: IHS Wireless
Bluetooth Low Energy Terms

**Bluetooth 4.0/4.1/4.2**
- Bluetooth 4.0 (2010) is an upgraded Bluetooth Classic specification that adds BLE
- Bluetooth 4.1 (2013) improves security, throughput and power consumption
- Bluetooth 4.2 (2014) increases packet length, and improves privacy and security

**PSoC 4 BLE**
- A 32-bit, 48-MHz ARM® Cortex™-M0 PSoC device with programmable subsystems: analog, digital, CapSense and a BLE radio
- Includes a royalty-free Stack compatible with Bluetooth 4.2

**PRoC BLE (Programmable Radio-on-Chip)**
- A 32-bit, 48-MHz ARM® Cortex™-M0 connectivity MCU with peripherals:
  - CapSense, ADC, SCBs and BLE
- Includes a royalty-free Stack compatible with Bluetooth 4.2
Bluetooth Low Energy Terms

BLE Component
- A PSoC Creator Component that creates Bluetooth Smart products in minutes
- Includes a Component Configuration Tool that makes the complex BLE Protocol Stack and Profiles simple to implement with a GUI

Over-The-Air (OTA) Firmware Upgrade
- The process of replacing an existing firmware with a newer version over a wireless interface like BLE
## Cypress’s BLE Supports ALL features of the Bluetooth 4.2 Standard

<table>
<thead>
<tr>
<th>Bluetooth 4.2 Feature</th>
<th>Benefit</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>LE¹ Data Length Extension</strong></td>
<td>Enabling ~2.5x higher throughput (up to 800 Kbps)…</td>
<td>For <strong>Faster OTA Upgrades</strong></td>
</tr>
<tr>
<td>Increases payload from 27 bytes to 251 bytes…</td>
<td></td>
<td>110 KB stack and application in 25 s</td>
</tr>
<tr>
<td><strong>LE¹ Privacy 1.2</strong></td>
<td>Enabling frequent address change with low power consumption…</td>
<td>For <strong>Privacy of User Data</strong></td>
</tr>
<tr>
<td>Moves address resolution from firmware to hardware…</td>
<td></td>
<td>Control visibility of wearables</td>
</tr>
<tr>
<td><strong>LE¹ Secure Connections</strong></td>
<td>Enabling enhanced security with interoperability…</td>
<td>For <strong>Secure Payment Solutions</strong></td>
</tr>
<tr>
<td>Uses FIPS-compliant ECDH as the key-generation algorithm for encryption…</td>
<td></td>
<td>Transfer passwords and financial information without worrying about being hacked</td>
</tr>
</tbody>
</table>
**Cypress’s BLE Enables Low-Power Wireless Systems**

<table>
<thead>
<tr>
<th>Power Mode</th>
<th>Current Consumption</th>
<th>Code Execution</th>
<th>Digital Peripherals Available</th>
<th>Analog Peripherals Available</th>
<th>Clock Sources Available</th>
<th>Wake-Up Sources</th>
<th>Wake-Up Time</th>
</tr>
</thead>
<tbody>
<tr>
<td>Active</td>
<td>2.2 mA @ 6 MHz</td>
<td>Yes</td>
<td>All</td>
<td>All</td>
<td>All</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Sleep</td>
<td>1.3 mA</td>
<td>No</td>
<td>All</td>
<td>All</td>
<td>All</td>
<td>Any interrupt source</td>
<td>0</td>
</tr>
<tr>
<td>Deep-Sleep</td>
<td>1.3 μA</td>
<td>No</td>
<td>WDT¹, LCD², I²C/SPI, Link-Layer³</td>
<td>Comparator, Opamps, POR⁴, BOD⁵</td>
<td>WCO⁶, 32-kHz ILO⁷</td>
<td>Comparator, GPIO⁸, WDT, SCB⁹</td>
<td>25 μs</td>
</tr>
<tr>
<td>Hibernate</td>
<td>150 nA</td>
<td>No</td>
<td>Comparator, POR, BOD</td>
<td>No</td>
<td>Comparator, GPIO</td>
<td>Wake-Up pin, XRES¹⁰</td>
<td>2 ms</td>
</tr>
<tr>
<td>Stop</td>
<td>60 nA</td>
<td>No</td>
<td>No</td>
<td>No</td>
<td>No</td>
<td>Wake-Up pin, XRES¹⁰</td>
<td>2 ms</td>
</tr>
</tbody>
</table>

**Cypress’s BLE has best-in-class low-power modes**
- Consumes the lowest current in Stop mode with GPIO retention
- Retains SRAM data in Hibernate mode
- Retains complete system status in Deep-Sleep mode
- Provides APIs to switch easily between low-power modes
- Consumes 17.1-μA avg. current for a 1-sec connection interval
Complete, End-to-end BLE Solution Set

Silicon

Modules

Software and Kits

Reference Designs

Silicon Modules

1.1 cm

1.1 cm

EZ-BLE PRoC Module

PSoC Creator IDE

Remote Control

QFN

1 cm

1 cm

EZ-BLE PSoC Module

BLE Pioneer Kit

Touch Mouse

CSP
Energy Harvesting
Terms You Will Hear Today

WSN
• Wireless Sensor Node

Energy Harvesting
• The process of capturing and converting tiny amounts of energy (e.g., from light, vibration or heat) into electricity

Energy Harvesting Power Management IC (PMIC)
• An IC that converts intermittent harvested power to stable power with low startup power (1.2 µW) and quiescent current\(^1\) (250 nA)

Energy Harvesting Device
• A device that harvests energy, e.g., solar cells, which harvest light; piezoelectric and electromagnetic induction devices, which harvest vibration; and thermoelectric generators, which harvest heat

Energy Storage Device
• A device that stores energy
• Low-leakage, low-impedance capacitors are commonly used for harvested energy storage—e.g., ceramic (100 µF), tantalum (470 µF), aluminum electrolytic (1,000 µF) or electric double-layer (500,000 µF) capacitors (e.g., supercapacitor)

\(^1\) Current consumed at no load condition
Terms You Will Hear Today

**Energy Harvesting System (EHS)**
- A system used to deliver power to a wireless sensor, or other low-power system, which includes an Energy Harvesting Device, a PMIC for energy conversion, and an Energy Storage Device
The WSN Device Market Is Set for Explosive Growth in the IoT

It will grow from 500M units in 2015 to more than 5B units in 2020¹

• Each WSN is a device in the Internet of Things (IoT) that communicates with servers on the Internet
• Each WSN monitors and reports on conditions in buildings, equipment and the environment

Energy Harvesting and low-power wireless communication enable WSN IoT Device market growth

• Next-generation WSNs will be powered by light, vibration or heat
• Next-generation WSNs will communicate over low-power BLE networks

The most compelling new WSNs are self-powered and can be deployed anywhere

• The WSN must last more than 10 years and require minimal deployment and maintenance costs

¹ Source: 802.15.4 & ZigBee: Enabling the Internet of Things by On World Inc., 2014
Applications

**WSNs for Building Automation**

- WSNs monitor conditions in buildings to reduce energy consumption.

**WSNs for Residential Control**

- WSNs monitor conditions in homes such as temperature and humidity.

**WSNs for Factory Automation**

- WSNs monitor factory pollution control to meet air quality standards.

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1 Source: 802.15.4 & ZigBee: Enabling the Internet of Things by On World Inc., 2014
Energy Harvesting PMIC: S6AE101A

**Applications**
- Series solar cell Energy Harvesting
- Wireless Sensor Node

**Features**
- Ultra-low power: Enables 1 cm² minimum solar cell size for startup operation
- Input voltage range:
  - Series solar cell: 2.0-5.5 V
  - Primary battery: 2.0-5.5 V
- Output voltage range: 1.1-5.2 V
- Quiescent current: 250 nA
- Startup power: 1.2 µW
- Power gating switch circuit
- Storage control circuit
- Multiplexer circuit (battery vs. solar cell)
- Overvoltage protection
- Packages: 10-pin SON (3.0 x 3.0 mm)

**Block Diagram**

(Schematic diagram showing the components and connections of the S6AE101A PMIC, including Primary Battery, Series Solar Cell, Multiplexer, Power Gating Switch, Storage Control, Overvoltage Protection, Control Block, and System Load.)
Low-Power, Tiny WSN Design

Combine a small solar cell, PMIC, and BLE radio...

Series solar cell, 1 cm², 2 µW @ 100 lux (lx)¹

S6AE101A Single-chip Energy Harvesting PMIC

EZ-BLE PRoC Module

To create the lowest-power WSNs powered by a tiny solar module.

Demonstration of a tiny 1-cm² solar-powered WSN using the Cypress S6AE101A Energy Harvesting PMIC Solution

¹ Per the manufacturer's datasheet.
Solar-Powered IoT Device Kit

The $49 kit contains a motherboard, solar module and a BLE-USB Bridge

The Energy Harvesting motherboard includes:
- S6AE101A Energy Harvesting PMIC
- EZ-BLE PRoC Module
- I2C sensor for temperature and humidity
- CY7C65213 USB-Serial device to program the BLE module firmware
- Ceramic capacitor (200 µF) for the Energy Storage Device
- Connector to interface to additional devices

The solar module includes:
- Series solar cell that produces 55 µW at 200 lux (lx) (Panasonic AM-1801)

The BLE-USB Bridge with PRoC BLE includes:
- An onboard LED, push button and connector for program and debug

The kit may be powered by several DC energy sources, including:
- Series solar cell (2.0 V-5.5 V)
- USB bus power (5-V) and coin cell (3-V)
Questions? Comments? Good (clean) jokes?
Appendices

- 4.2 LE Authentication and Privacy
- BLE Mesh Introduction
- BLE 5.0 Sneak Peek
BLE 4.2: LE Secure Connections: Authentication

Authentication phase is the key difference between Bluetooth 4.1 and Bluetooth 4.2 (LE Secure connections).

Bluetooth 4.1: The Temporary Key (used to derive the key that encrypts the BLE link) is the only random data not exchanged over the BLE link. As all other keys are exchanged over air, eavesdropper can decipher temporary key and hack the link.

Bluetooth 4.2: In LE Secure connections, initiating device and responding device exchange their public keys and start computing Diffie-Hellman key. This key is never exchanged over air.
## Bluetooth 4.1 and 4.2 Privacy

<table>
<thead>
<tr>
<th>Feature</th>
<th>4.1</th>
<th>4.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Address Generation timeout</td>
<td>Recommended time 15mins</td>
<td>Host programmable 1sec-11.5 hours</td>
</tr>
<tr>
<td>RPA Resolution</td>
<td>in the Host</td>
<td>in the Controller</td>
</tr>
<tr>
<td>RPA Generation</td>
<td>in the Host</td>
<td>in the Controller</td>
</tr>
<tr>
<td>Directed Advertisement</td>
<td>Cannot be used for connections with RPA</td>
<td>Can be used for connections with RPA</td>
</tr>
<tr>
<td>Whitelist filtering</td>
<td>Cannot be used for connections with RPA</td>
<td>Can be used for connections with RPA</td>
</tr>
<tr>
<td>Resolving List</td>
<td>Maintained in the host</td>
<td>Maintained in the Controller. Host adds/deletes devices in the list.</td>
</tr>
</tbody>
</table>

RPA = Resolvable Private Address
Introduction to BLE Mesh

BLE Mesh Network (earlier termed as Smart Mesh) is a mesh networking protocol over BLE defined by the Bluetooth SIG, to transfer data from one device to another either directly or by relaying through intermediate devices.

Two versions of BLE Mesh

- **BLE Mesh V1** is under development and expected to be released by end of 2016. It is based on flooding\(^1\) mechanism. Due to its simplicity, its development and adoption to market will be fast.
- **BLE Mesh V2** is expected to be released by end of 2017. It will be based on routing\(^2\) mechanism. This protocol is expected to be backward compatible with BLE Mesh V1. Though complex in implementation, it will optimize network utilization and power consumption.

BLE Mesh V1 Features:

- Primarily transfers data using advertisement (all of the 3 advertisement channels)
- Extends range of BLE communication by relaying data through intermediate nodes
- Securely communicates message between nodes and minimizes security threats
- Provides GATT connectivity for legacy devices that cannot communicate mesh packets over advertisement
- Specification is under development (at 0.7) with most of the features finalized

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\(^1\) Flooding is a simple process where every incoming packet is sent out (relay).

\(^2\) Routing is a process where a device decides on certain conditions before it relays the packet.
Bluetooth 5.0 Sneak Peek

LE Advertising Extension
- Increases length of the BLE advertisement packet. Allows usage of all 40 channels for advertisement

LE 2 Mbps PHY
- Increases the data rate supported by BLE

Long Range
- Forward Error Correction (FEC) to increase receive sensitivity and therefore range
- Data is encoded with 2-bit or 8-bit codes in FEC to increase the amount of bit errors that can be tolerated
Bluetooth 5.0 - Connection Oriented AoA (Angle of Arrival) and Connectionless AoD (Angle of Departure) for Direction Finding

- Tracker is the device that does the calculation of AoA or AoD
- Target is the device being tracked
- Antenna diversity and analysis of I & Q\(^1\) baseband signals are used to determine the direction of the target

\(^1\) Quadrature signals or signals that differ in phase by 90 degrees
The End. Sleepy-Sleepy Nighttime!