IEEE SF Bay Area MEMS and Sensor Chapter and Semiconductor Equipment and Materials International (SEMI) present a joint workshop on MEMS Standards – Current Status and Future

**Date:** Oct. 28th, 2015 (Wednesday)
**Time:** 6:30 PM to 8:30 PM
**Location:** Texas Instruments, Building E Conference Center, 2900 Semiconductor Dr., Santa Clara, CA 95052.

The first MEMS accelerometer was developed in the late 1970’s at Stanford University, but it took an additional 15 years before the automotive industry adopted the technology for air bag safety systems. Since then, applications for MEMS accelerometers have grown into smart phone, laptop computers, mobile electronics, game controllers, and wearable electronic markets.

Standards help in defining terminology, materials, test methods, performance specifications, and device interoperability. Moreover, standards play a critical role in the successful commercialization of equipment, processes, products, and services. Multiple organizations are responsible for creating the existing, albeit the small set of MEMS Standards. The industry is calling for additional MEMS standards to help increase rates of adoption and future growth of MEMS technologies. In the first part of this workshop, experts will present the role and current status of MEMS Standards. The second part of the workshop will feature an interactive panel session on the future of MEMS Standards.

Please join us and help us shape the future of MEMS standards!

6:30 – 7:30 PM
**Session I: Current Status of MEMS Standards**
James Amano - SEMI
Lloyd Green – IEEE
Mary Ann Maher – GSA
Rich Allen - NIST

Break (15 mins)

7:45 - 8:30 PM
**Session II: Future of MEMS Standards**
Interactive Panel discussion (45 mins)